

PREPARED BY: _____	DATE _____	<h1 style="margin: 0;">S H A R P</h1> <p style="margin: 0;">LIQUID CRYSTAL DISPLAY GROUP SHARP CORPORATION</p> <h2 style="margin: 0;">SPECIFICATION</h2>	SPEC No. <b>LD7807</b>
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			APPLICABLE GROUP Liquid Crystal Display Group

DEVICE SPECIFICATION FOR

# TFT-LCD module

MODEL No.

## LQ9 P 341

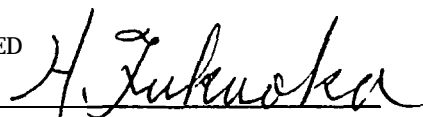
CUSTOMER'S APPROVAL

DATE \_\_\_\_\_

BY \_\_\_\_\_

PRESENTED

BY



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Department General Manager

Engineering Department 2

TFT LCD Development Center

LIQUID CRYSTAL DISPLAY GROUP

SHARP CORPORATION



## 1. Application

This specification applies to a color TFT-LCD module, LQ9P341.

## 2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFTs (Thin Film Transistors). This module is composed of a high-transmissivity-type color TFT-LCD panel, driver ICs, control circuit and power supply circuit. Graphics and texts can be displayed on a 640x3x480 dots panel with 262,144 colors by supplying 18 bit data signals (6bit/color), four timing signals, +5V DC supply voltage for TFT-LCD panel driving.

### [Features]

- ◎ Low power consumption.
- ◎ High transmissivity.

## 3. Mechanical Specifications

Parameter	Specifications	Unit
Display size	21 (8.4") Diagonal	cm
Active area	170.9(H) × 129.6(V)	mm
Pixel format	640(H) × 480(V)	pixels
	(1 pixel = R + G + B dots)	
Pixel pitch	0.267(H) × 0.270(V)	mm
Pixel arrangement	R, G, B vertical stripe	
Display mode	Normally white	
Unit outline dimensions	242.5(W) X 179.4(H) x 7.5(D)	mm
Mass	330 ± 20	g
Surface treatment	Clear, hard-coating(2H)	

The outline dimensions is shown in Fig.1

## 4. Input Terminals

## 4-1. TFT-LCD panel driving

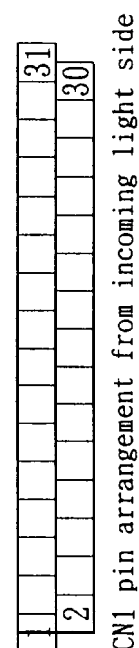
The module-side connector: DF9M-31P-1V (Hirose Electric Co., Ltd.)

CN1

The user-side connector: DF9□-31S-1V ( " ), □ is blank, A or B.

Pin No.	Symbol	Function	Remark
1	GND		
2	CK	Clock signal for sampling each data signal	
3	Hsync	Horizontal synchronous signal	【Note1】
4	Vsync	Vertical synchronous signal	【Note1】
5	GND		
6	R0	RED data signal (LSB)	
7	R1	RED data signal	
8	R2	RED data signal	
9	R3	RED data signal	
10	R4	RED data signal	
11	R5	RED data signal (MSB)	
12	GND		
13	Go “	GREEN data signal (LSB)	
14	G1	GREEN data signal	
15	G2	GREEN data signal	
16	G3	GREEN data signal	
17	G4	GREEN data signal	
18	G5	GREEN data signal (MSB)	
19	GND		
20	B0	BLUE data signal (LSB)	
21	B1	BLUE data signal	
22	B2	BLUE data signal	
23	B3	BLUE data signal	
24	B4 “	BLUE data signal	
25	B5	BLUE data signal (MSB)	
26	GND		
27	ENAB	Signal to settle the horizontal display position	【Note2】
28	Vcc	+5V power supply	
29	Vcc	+5V power supply	
30	R/L	Horizontal display mode select signal	【Note3】
31	U/D	Vertical display mode select signal	【Note3】

※The shielding case is connected with GND in the module.

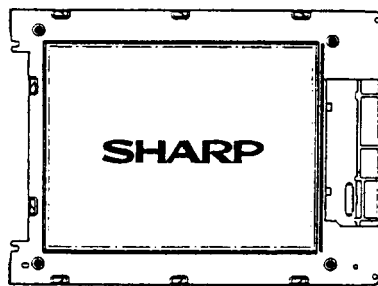


【Note1】 One of 480-, 400- and 350-line mode is selected depending on the polarity combination of the both synchronous signals.

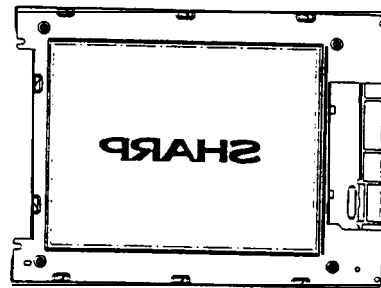
mode	480-line	400-line	350-line
Hsync	negative	negative	positive
Vsync	negative	positive	negative

【Note2】 The horizontal display start timing is settled in accordance with a rising edge of ENAB signal. In case ENAB is fixed “Low”, the horizontal display start timing is determined as described in 7-2. Do not keep ENAB “High” during operation.

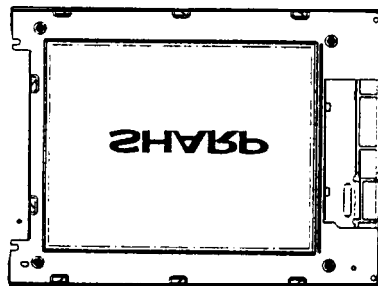
[Note31]



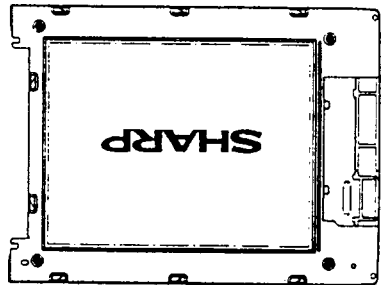
R/L=L, U/D=H



R/L=H, U/D=H



R/L=L, U/D=L



R/L=H, U/D=L

※ Do not use R/L and U/D in disconnection condition,

### 5. Absolute Maximum Ratings

Parameter	Symbol	Conditio	Ratings	Unit	Remark
Input voltage	$v_i$	$T_a=25^\circ\text{C}$	$-0.3 \sim V_{cc}+0.3$	v	【Note1】
Vcc supply voltage	$V_{cc}$	$T_a=25^\circ\text{C}$	$0 \sim +6$	v	
Storage temperature	$T_{stg}$	—	$-25 \sim +60$	'c	【Note2】
Operating temperature (Ambient)	$T_{opa}$	—	$0 \sim +50$	"c	
Panel surface temperature	$T_p$	—	$0 \sim +60$	"c	
Light source wave length	$\lambda_1$	—	$\geq 400$	nm	
Light source illumination intensity	$I_1$	—	$\leq 300,000$	lx	【Note3, 4】

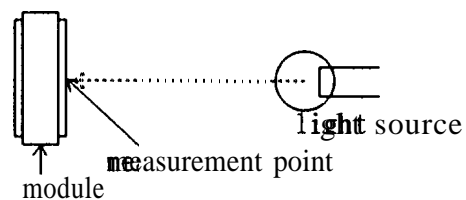
【Note1】 CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, ENAB, R/L, U/D

【Note2】 Humidity :95%RH Max. at  $T_a \leq 40^\circ\text{C}$ .

Maximum wet-bulb temperature at  $39^\circ\text{C}$  or less at  $T_a > 40^\circ\text{C}$ .

No condensation.

【Note3】 Measurement point: panel surface



【Note4】 Light source shall be placed at incoming light side. (see fig.1)

6. Electrical Characteristics

6-1. TFT-LCD panel driving

Ta=25°C

Parameter		Symbol	Min.	Typ.	Max.	Unit	Remark
Vcc	Supply voltage	Vcc	+4.5	+5.0	+5.5	v	【Note1】
	Supply current	Icc	“	160	360	m A	【Note2】
Permissive input ripple voltage		V <sub>RP</sub>	-	-	100	mVp-p	Vcc
Input voltage (Low)		V <sub>IL</sub>	-	-	0.3VCC	V	【Note3】
Input voltage (High)		V <sub>IH</sub>	0.7VCC	-	-	V	
Input current (low)		I <sub>IL1</sub>	-	-	1.0	μ A	V <sub>i</sub> =0V 【Note4】
		I <sub>IL2</sub>	2.0	-	30.0	μ A	V <sub>i</sub> =0V 【Note5】
Input current (High)		I <sub>IH1</sub>	-	-	1.0	μ A	V <sub>i</sub> =Vcc 【Note6】
		I <sub>IH2</sub>	2.0	-	30.0	μ A	V <sub>i</sub> =Vcc 【Note7】

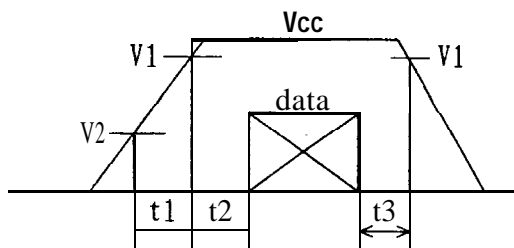
【Note1】

On-off condition for supply voltage

0 < t1 ≤ 10msec

0 < t2 ≤ 50msec

0 < t3 ≤ 1sec

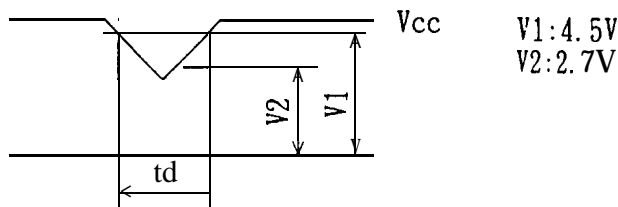


Vcc-dip conditions

1) V2 ≤ Vcc < V1

t<sub>d</sub> ≤ 10msec

2) Vcc < V2



Vcc-dip conditions should also follow the Vcc-turn-on conditions

【Note2】 The typical value of Icc is measured in the following condition.

64-gray-bar pattern.

All of the timing parameters are typical value (480 line mode).

Vcc=+5V

RGB	RGB	RGB	RGB	RGB	RGB	RGB
GS0	GS1	GS2	→	GS61	GS62	GS63

【Note3】 CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, ENAB, R/L, U/D

【Note4】 CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, ENAB

【Note5】 R/L, U/D

【Note6】 CK, R0~R5, G0~G5, B0~B5, Hsync, Vsync, R/L, U/D

【Note7】 ENAB

## 7. Timing Characteristics of input signals

The timing diagrams of the input signals are shown in Fig.2-①~③.

### 7-1. Timing characteristics

Parameter		Symbol	Mode	Min.	Typ.	Max.	Unit	Remark
Clock	Frequency	1/Tc	all	—	25.18	28.33	MHz	
	High time	Tch	"	5	-	-	ns	
	Low time	Tcl	"	10	-	-	ns	
Data	Setup time	Tds	"	5	-	-	ns	
	Hold time	Tdh	"	10	-	-	ns	
Horizontal sync. signal	Period	TH	"	30.00	31.78	-	μs	
			"	770	800	900	clock	
	Pulse width	THp	"	2	96	200	clock	
Vertical sync. signal	Period	TV	480	515	525	560	line	
			400	445	449	480	line	
			350	447	449	510	line	
	Pulse width	TVp	all	2	-	34	line	
Horizontal display period		THd	"	640	640	640	clock	
Hsync-Clock phase difference		THc	"	10	-	Tc-10	ns	
Hsync-Vsync phase difference		TVh	"	0	-	TH-THp	clock	

Note) In case of lower frequency, the deterioration of the display quality, flicker etc. , may be occurred.

### 7-2. Horizontal display position

The horizontal display position is determined by ENAB signal and the input data corresponding to the rising edge of ENAB signal is displayed at the left end of the active area.

Parameter		Symbol	Mode	Min.	Typ.	Max.	Unit	Remark
ENAB signal	Setup time	Tes	all	5	-	Tc-10	ns	
	Pulse width	Tep	"	2	640	640	clock	
Hsync-ENAB signal phase difference		THE	"	44	-	164	clock	

Note) When ENAB is fixed "Low", the display starts from the data of C104(clock) as shown in Fig.2-①~③. Be careful that the module do not work when ENAB is fixed "High".

7-3. Vertical display position

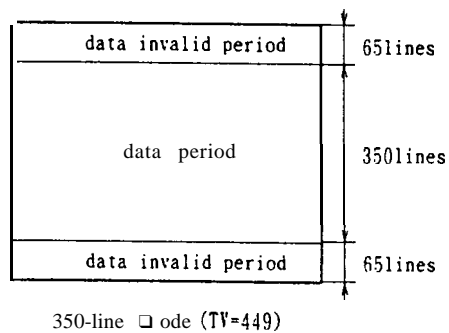
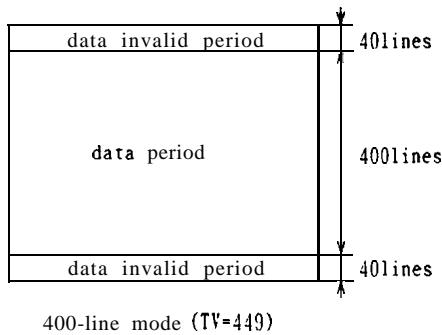
The vertical display position is automatically centered in the active area at each mode of VGA, 480-, 400- and 350-line mode. Each mode is selected depending on the polarity of the synchronous signals described in 4-1 (Notel).

In each mode, the data of TVn is displayed at the top line of the active area. And the display position will be centered on the screen like the following figure when the period of vertical synchronous signal, TV, is typical value.

In 400- and 350-line mode, the data in the vertical data invalid period is also displayed. So, inputting all data "O" is recommended during vertical data invalid period.

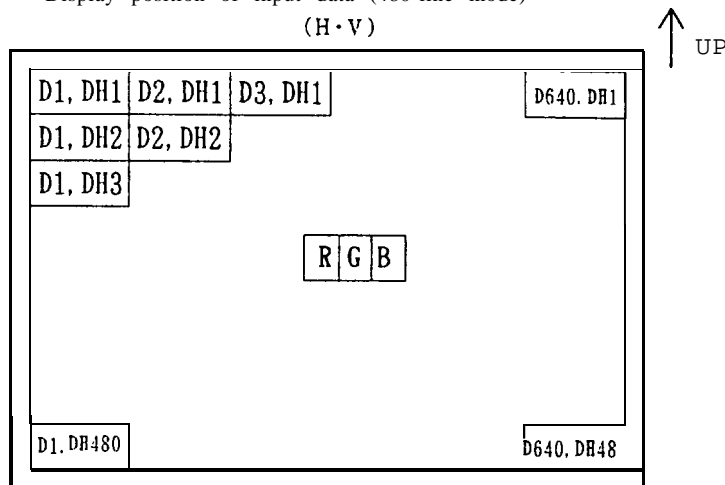
ENAB signal has no relation to the vertical display position.

mode	V-data start(TVs)	V-data period(TVd)	V-display start(TVn)	Unit	Remark
480	34	480	34	line	
400	34	400	443-TV	line	
350	61	350	445-TV	line	



7-4, Input Data Signals and Display Position on the screen

Display position of input data (480-line mode)  
(H·V)





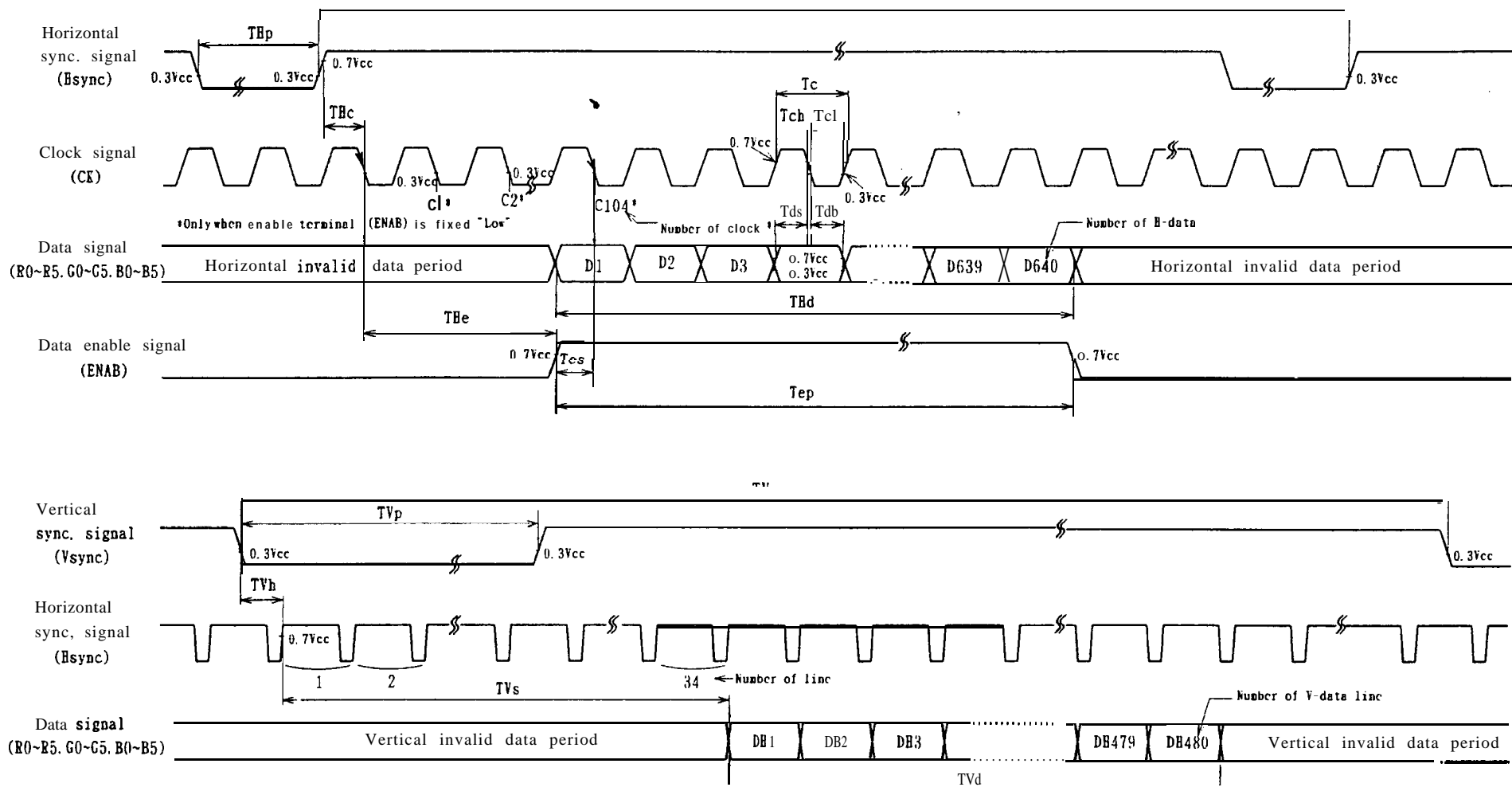


Fig. 2-① Input signal waveforms (480-line mode)

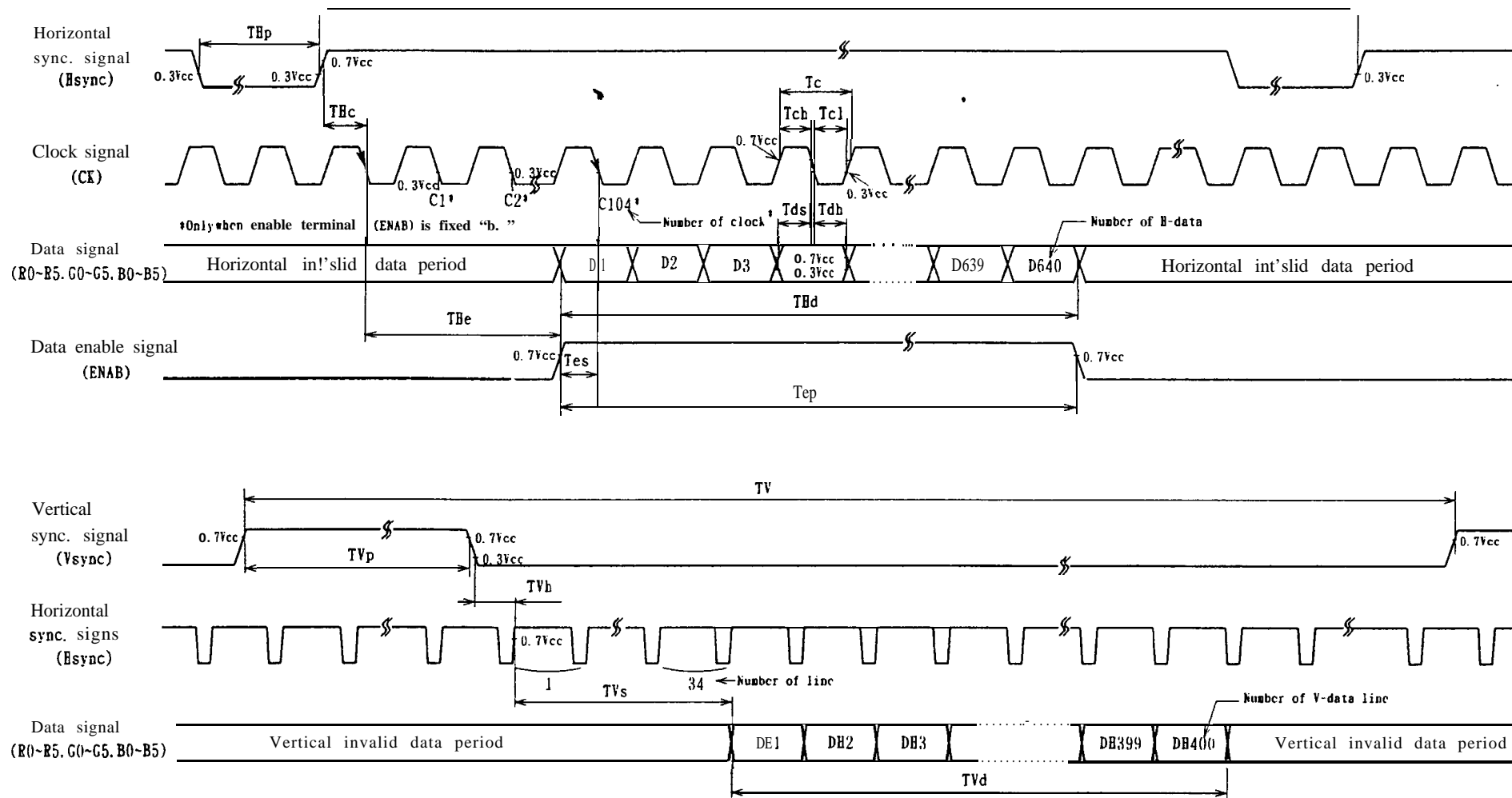


Fig. 2-② Input signal waveforms (400-line mode)

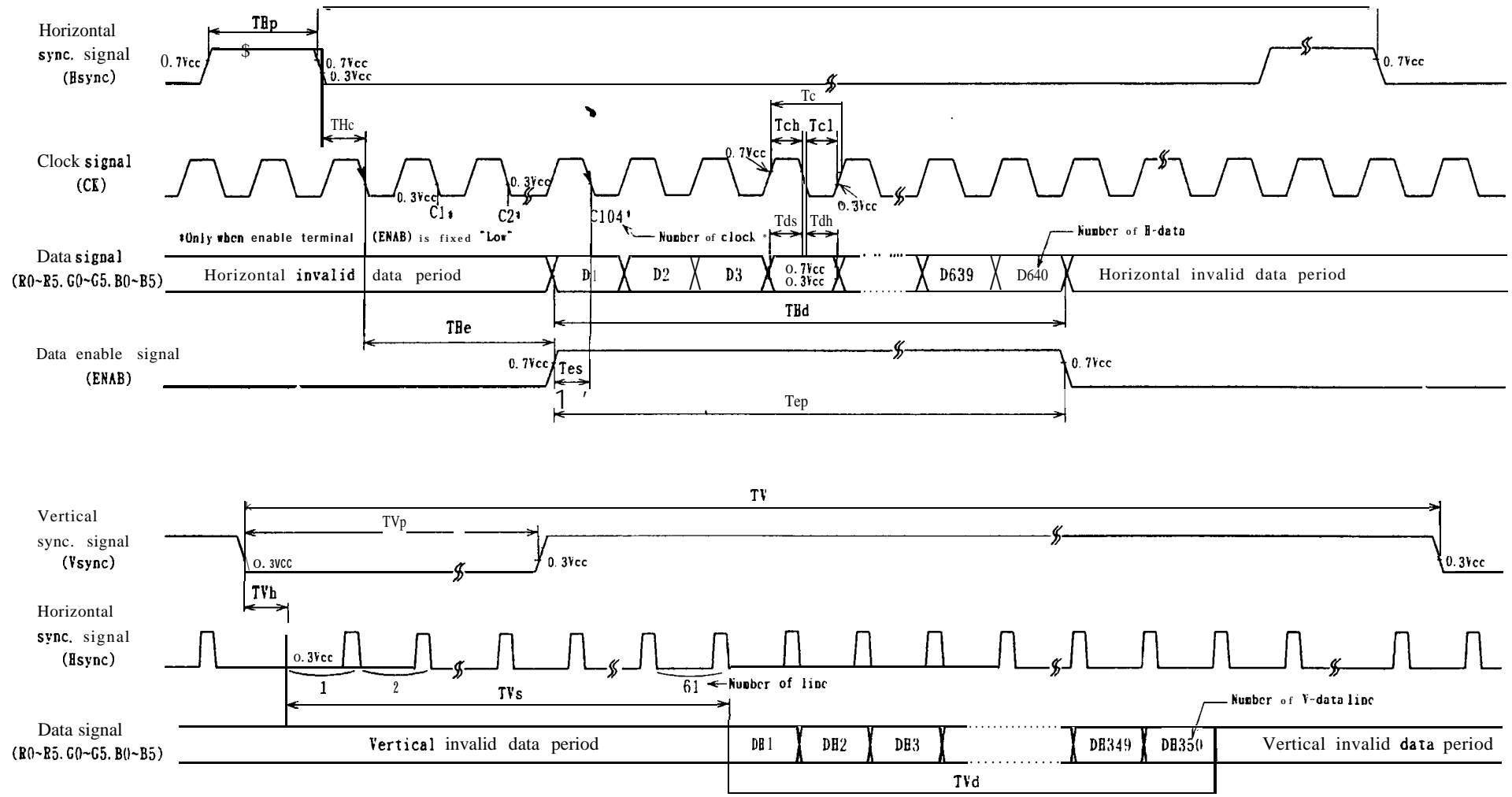


Fig. 2-③ Input signal waveforms (350-line mode)

## 8. Input Signals, Basic Display Colors and Gray Scale of Each Color

	Color & Gray		Data signal (0: Low level voltage, 1: High level voltage)																	
	scale(GS)	GS	RO	R1	R2	R3	R4	R5	GO	G1	G2	G3	G4	G5	BO	B1	B2	B3	B4	B5
Basic color	Black	“	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	-	0	0	0	0	0	0	00	0	00	0			1	1	1	1	1	1
	Green	-	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Light blue	-	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Red	-	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Purple	-	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
	Yellow	-	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Gray Scale of Red	Black	GS0	0	0	0	0	0	0	00	0	00	0			0	0	0	0	0	0
	↑	GS1	1	0	0	0	0	0	00	0	00	0			0	0	00	00		
	Darker	GS2	0	1	0	0	0	0	00	0	00	0			0	0	0	0	0	0
	↑	↓																		
	u “	↓																		
	Brighter	GS61	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	↓	GS62	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Red	GS63	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	
Gray Scale of Green	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	↑	GS1	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
	↑	↓																		
	↓	↓																		
	Brighter	GS61	0	0	0	0	0	0	1	0	1	1	1	1	0	0	0	0	0	0
	↓	GS62	0	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0
Green	GS63	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0	
Gray Scale of Blue	Black	GS0	0	0	0	0	0	0	00	0	00	0			0	0	0	0	0	0
	↑	GS1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
	↑	↓																		
	↓	↓																		
	Brighter	GS61	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1
	↓	GS62	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1
Blue	GS63	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	

Each basic color can be displayed in 64 gray scales from 6 bit data signals.

According to the combination of total 18 bit data signals, the 262,144-colors display can be achieved on the screen.

## 9. Optical Characteristics

Ta=25℃, Vcc=+5V

Parameter		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark
Viewing angle range	Horizontal	$\theta 21, 22$	CR>10	35	-	-	Deg.	【Note1, 5】
	Vertical	$\theta 11$		30	-	-	Deg.	
		e 12		10	-	-	Deg.	
Contrast ratio		CR	$\theta = 0^\circ$	130	-	-		[Note2, 5]
Response time	Rise	$\tau r$		-	20	-	ms	【Note3, 5】
	Decay	$\tau d$		-	40	-	ms	
Transmissivity		t r		6.5	7.5	-	%	[Note4, 5]
Chromaticity shift	$\Delta x$			-0.030	-	0.045		[Note5, 6]
	$\Delta y$			0.005	-	0.065		

[ Optical characteristics of backlight for measurement ]

luminance :  $\geq 3500\text{cd/m}^2$ wave length :  $\geq 400\text{nm}$ 

Note) The optical characteristics shall be measured in a dark room or equivalent state with the method shown in Fig. 3.

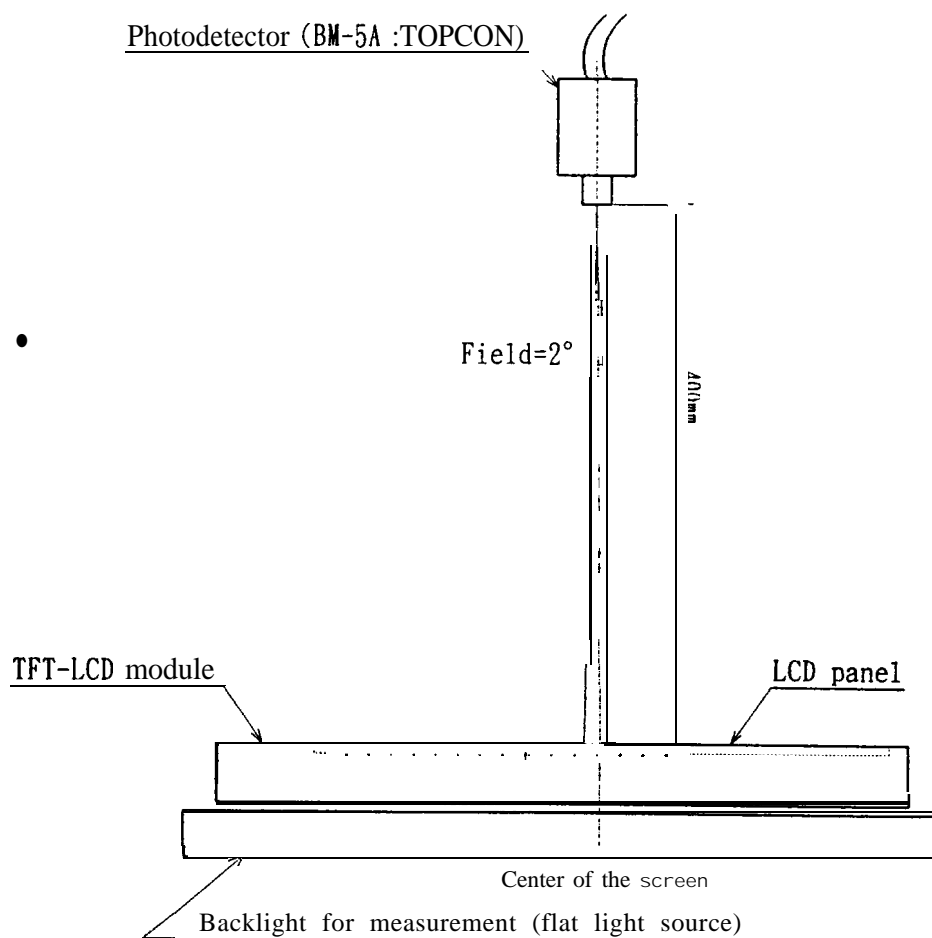
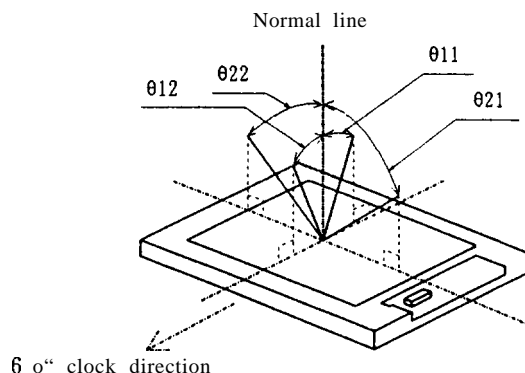


Fig.3 Optical characteristics measurement method

**【Note1】** Definitions of viewing angle range:



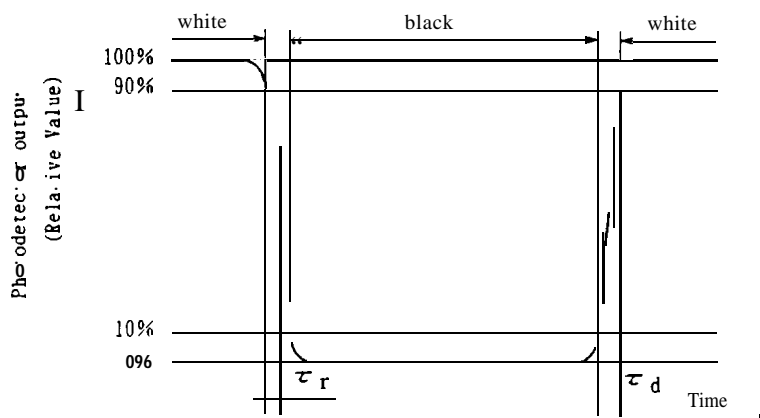
**【Note2】** Definition of contrast ratio:

The contrast ratio is defined as the following.

$$\text{Contrast Ratio (CR)} = \frac{\text{Luminance (brightness) of "white"}}{\text{Luminance (brightness) of "black"}}$$

**【Note3】** Definitions of response time:

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



**【Note4】** Definitions of Transmissivity:

$$\text{Transmissivity} = \frac{\text{light detected level of the transmission through the LCD panel}}{\text{light detected level of the Original light source}}$$

**【Note5】** The values are measured at center of the screen.

**【Note6】** Chromaticity shift is the difference of those of those of the light source and the module placed on it.

The values are measured with standard illuminant: C ( $x=0.310, Y=0.316$ )

## 10. Display Quality

The display quality of the color TFT-LCD module shall be in compliance with the Incoming Inspection Standard.

## 11. Handling Precautions

11-1) Be sure to insert the cable into the connector or take out of the connector after turning off the power supply on the set side.

11-2) Precautions in mounting

- a) When installing the module, be sure to fix the module on the same plane, taking care not to warp or twist the module.
- b) Since the polarizer is made of soft material, care must be taken not to scratch the surface. Protective laminated film is attached on the outgoing light side surface glass to protect from scratches or dirt. It is recommended that the laminated film is peeled off, just before the use, with strict attention to electrostatic charges.

※ Precautions when peeling off the laminated film:

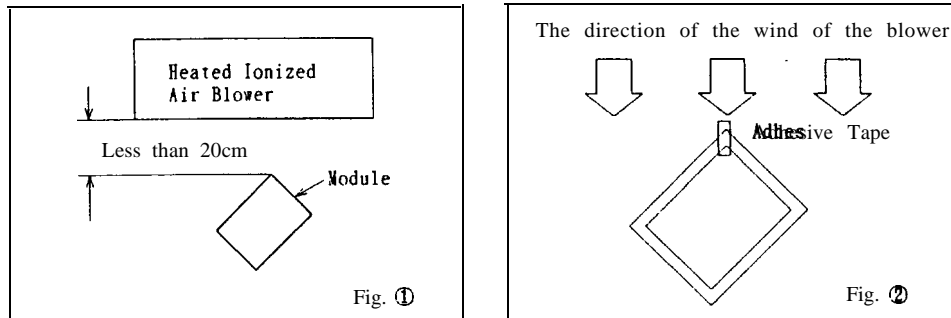
### I) Working environment

When the laminated film is peeled off, there may be cases that some particles like dust are stuck by electrostatic charges, so the following working environment is recommended.

- (i) Floor: Anti-electro-static treatment more than 1M $\Omega$  on the tile.
- (ii) Spread an adhesive mat at a doorway in the clean room.
- (i) Humidity: 50% to 70%, Temperature: 15 $^{\circ}$ C to 27 $^{\circ}$ C
- (iv) Worker needs anti-electro-static shoes, anti-electro-static work wear, anti-electro-static gloves and earth band.

### II) Working way

- (i) Keep the distance between the module and the heated ionized air blower within 20cm. The module shall be well blown to the wind of the blower. (Fig. ①)
- (ii) Attach an adhesive tape to a corner of the laminated film near the heated ionized air blower. (Fig. ②)
- (m) Peel the laminated film pulling the adhesive tape to your side. It is important that it takes more than 5 seconds to peel off the laminated film.
- (iv) The module after peeling laminated film must be moved to next work immediately without getting dust.
- (v) The way to remove 'dust' from the polarizer
  - Blow it off by nitrogen blow that is taken measures against electrostatic charges. Ionized air gun is recommended.
  - Since polarizer is easily damaged, In unavoidable case, wipe it carefully by the cloth like a wiper for lens, breathing on it.



### 11-3) Others

- a) Wipe off water drop immediately. Long contact with water may cause discoloration or spots.
- b) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- c) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with care.
- d) Since CMOS LSI is used in this module, take care of static electricity and ground your body when handling.
- e) Observe all other precautionary requirements in handling components.

### 12. Packing form

- a) Piling number of cartons : MAX. 7
- b) Package quantity in one carton : MAX. 10
- c) Carton size : 413(W) $\times$ 288(H) $\times$ 351(D)mm
- d) Total mass of 1 carton filled with full modules : 6000g

Packing form is shown in Fig. 4.



## 13. Reliability test items

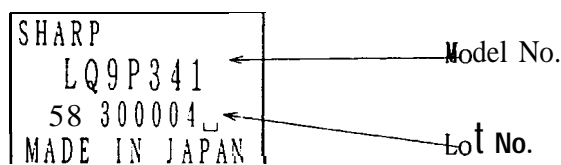
No.	Test item	Conditions
1	High temperature storage test	Ta=60℃ 240h
2	Low temperature storage test	Ta=-25℃ 240h
3	High temperature & high humidity operation test	Ta=40℃;95%RH 240h (No condensation)
4	High temperature operation test	Ta=50℃ 240h
5	Low temperature operation test	Ta=0℃ 240h
6	Vibration test (non-operating)	Frequency:10~57Hz/Vibration width (one side):0.075m :58~500Hz/Gravity:9.8m/s <sup>2</sup> Sweep time: 11 minutes Test period:3 hours (1 hour for each direction of X, Y, Z)
7	Shock test (non-operating)	Max. gravity:490m/s <sup>2</sup> Pulse width: 11ms, sine wave Direction: ±X, ±Y, ±Z once for each direction.

## [Result Evaluation Criteria]

Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function.

## 14. Others

## 1) Lot No. Label



- 2) Adjusting volumes have been set optimally before shipment, so do not change any adjusted values. If adjusted values are changed, the data mentioned in this specification may not be satisfied.
- 3) Disassembling the module can cause permanent damage and should be strictly avoided.
- 4) Please be careful since image retention may occur when a fixed pattern is displayed for a long time.
- 5) If any problem occurs in relation to the description of this specification, it shall be resolved through discussion with spirit of cooperation.

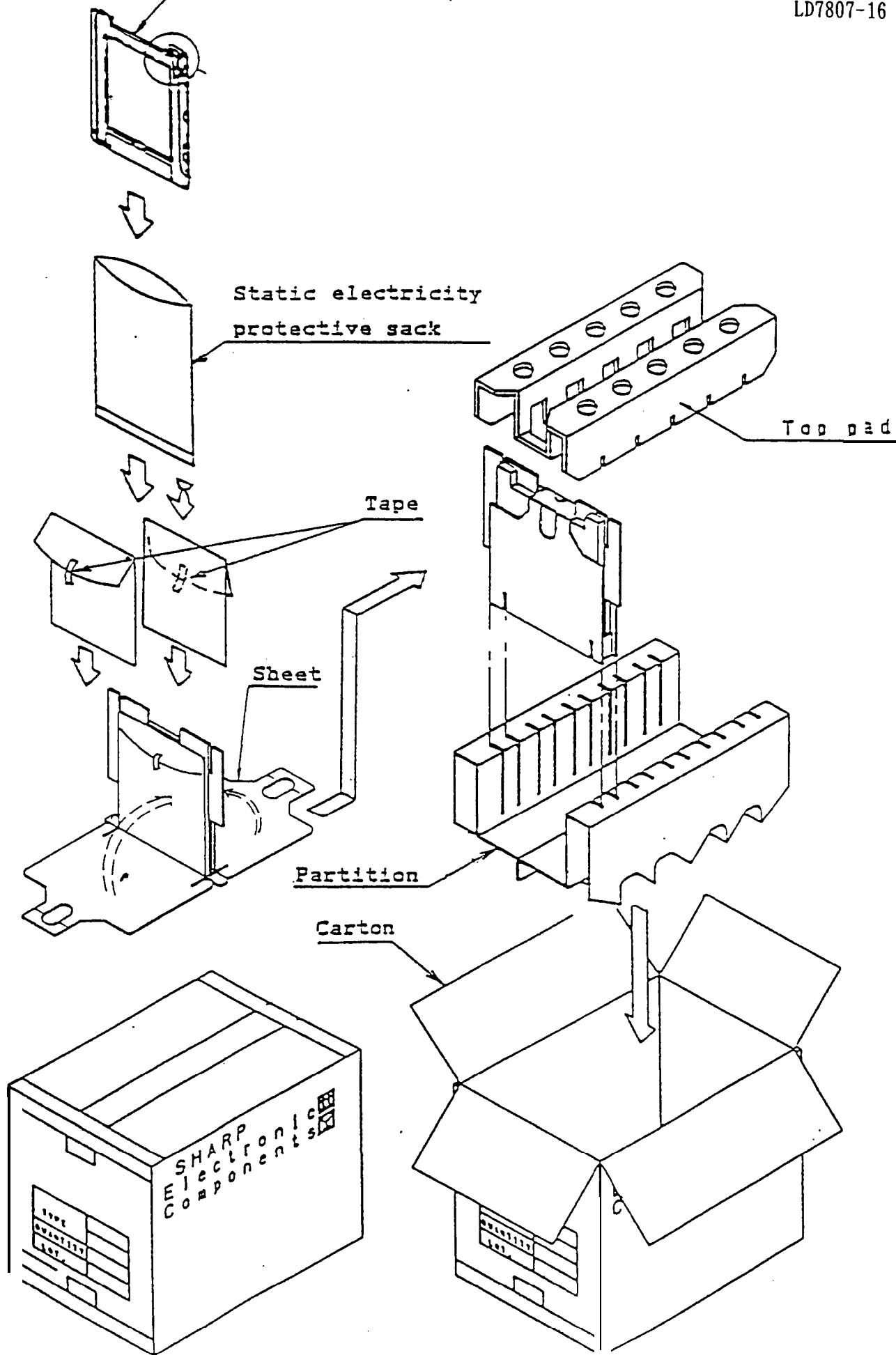


Fig. 4 Packing form

FIG. 1 OUTLINE DIMENSIONS

NOTES  
 Unspecified tolerance shall be  $\pm 0.5$   
 Value in ( ) is just for reference  
 CN1:DF9-31P-1V (HIROSE)  
 \*These holes ( $\phi 3$ ) can be used for aligning this module to your product.

